



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re U.S. Patent Application of )  
YASUI et al. )  
Application Number: 09/910,904 )  
Filed: July 24, 2001 )  
For: PLANARIZING METHOD OF SEMICONDUCTOR )  
WAFER AND APPARATUS THEREOF )

**Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231**

**RESPONSE TO NOTICE TO FILE MISSING PARTS**

Sir:

This is in response to the Notice of Missing Parts dated September 19, 2001, of the Initial Patent Examination Division of the Patent and Trademark Office, the period for response to which is set to expire on November 19, 2001. Submitted herewith is an English translation of the application for the above-captioned application.

The fee of \$130.00 is enclosed. Please direct any inquiries in connection with this application to the undersigned.

Respectfully submitted,

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**September 24, 2001**

09/24/01 10:50:40